

This ZIP file contains 4 executables which should all be grouped together in the same directory.

The Launcher application is used to select, via a browse function, the *circuit.lar* file containing a “spice-like” netlist describing the circuit to be analysed. Having selected a *circuit.lar* file the ‘Tran’ button should be clicked to translate the spice-like netlist into a spacetime netlist i.e. *circuit.net* which is the input format for the Largess simulator itself.

*NOTE: an inductor needs an associated resistor through which its current can be sensed, if there is not an appropriate resistor already in the circuit a small resistor must be added. The inductor definition takes the form:-*

*r12 5 0 1.0e-3*

*l2 3 5 35.0e-9*

*with the resistor definition immediately preceding the inductor definition.*

Following the creation of a *circuit.net* file, it can be selected in the Launcher browse function to run any number of subsequent analyses by clicking the Largess button. Any (Dirichlet) boundary conditions voltage/current sources can be specified at run-time. Output files are *circuit.var* files (var for variable) which can be analysed by clicking the ‘Post’ button for a given *circuit.net* file. The particular type of output (absolute or relative nodal voltage, Fourier analyses etc) is specified at run-time.